

EXPEDITED PROCEDURE-EXAMINING GROUP 2192

S/N 10/676,141

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Peramachanahalli S. Ramkumar et al.	Examiner:	Thuy Dao
Serial No.:	10/676,141	Group Art Unit:	2192
Filed:	September 30, 2003	Docket No.:	884.A45US1
Customer No.:	45457	Confirmation No.:	6415
Title:	COMBINED EMULATION AND SIMULATION DEBUGGING TECHNIQUES		

RESPONSE UNDER 37 C.F.R. 1.116

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In response to the Final Office Action mailed July 28, 2008, please see the remarks that follow.

OK TO ENTER. TDAO 10/09/08